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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

72943

APP. NUM 10064301	FILING DATE 07/01/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER THA1.1.1
<b>**APPLICANTS:</b> Brown Jeffrey; Mann Randy;					
<b>**CONTINUING DATA VERIFIED:</b> None TN					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> None TN					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO BUR920010185	
Verified and Acknowledged Examiners's initials		TN			
TITLE : Method and structure for scalable, low-cost polysilicon capacitor in a planar dram					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	
		Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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